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# SBAV99LT1G

## onsemi

Diodes - General Purpose, Power, Switching SS DUAL DIO SPCL TR

Any questions, please feel free to contact us. info@kaimte.com

## BAV99L, SBAV99L

# **Dual Series**Switching Diode

#### **Features**

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS (Each Diode)

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	100	Vdc
Forward Current	I <sub>F</sub>	215	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc
Repetitive Peak Reverse Voltage	$V_{RRM}$	100	V
Average Rectified Forward Current (Note 1) (averaged over any 20 ms period)	I <sub>F(AV)</sub>	715	mA
Repetitive Peak Forward Current	I <sub>FRM</sub>	450	mA
Non–Repetitive Peak Forward Current t = 1.0 μs t = 1.0 ms t = 1.0 s	I <sub>FSM</sub>	2.0 1.0 0.5	A

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

- 1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina =  $0.4 \times 0.3 \times 0.024$  in 99.5% alumina.

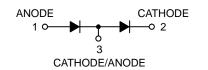


#### ON Semiconductor®

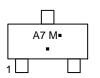
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CASE 318 SOT-23 STYLE 11



#### **MARKING DIAGRAM**



A7 = Device Code
M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
BAV99LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SBAV99LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BAV99LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
SBAV99LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

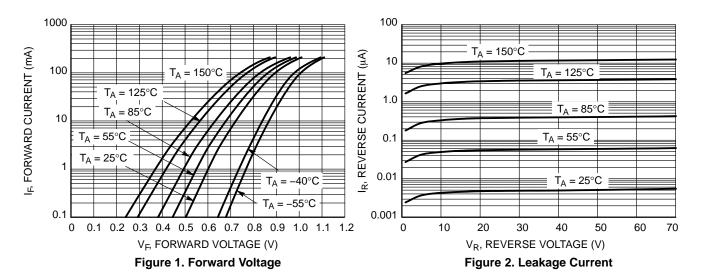
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### BAV99L, SBAV99L

#### **OFF CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted) (Each Diode)

Characteristic	Symbol	Min	Max	Unit
Reverse Breakdown Voltage, (I <sub>(BR)</sub> = 100 μA)	V <sub>(BR)</sub>	100	-	Vdc
Reverse Voltage Leakage Current, $(V_R = 100 \text{ Vdc})$ $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I <sub>R</sub>	- - -	1.0 30 50	μAdc
Diode Capacitance, (V <sub>R</sub> = 0, f = 1.0 MHz)	C <sub>D</sub>	_	1.5	pF
Forward Voltage, (I <sub>F</sub> = 1.0 mAdc) (I <sub>F</sub> = 10 mAdc) (I <sub>F</sub> = 50 mAdc) (I <sub>F</sub> = 150 mAdc)	V <sub>F</sub>	- - - -	715 855 1000 1250	mVdc
Reverse Recovery Time, (I <sub>F</sub> = I <sub>R</sub> = 10 mAdc, $i_{R(REC)}$ = 1.0 mAdc) R <sub>L</sub> = 100 $\Omega$	t <sub>rr</sub>	_	6.0	ns
Forward Recovery Voltage, (I <sub>F</sub> = 10 mA, t <sub>r</sub> = 20 ns)	V <sub>FR</sub>	_	1.75	V

#### **CURVES APPLICABLE TO EACH DIODE**



0.61 0.59 0.57 0.55 0.53 0.51 0.47 0.45 0 1 2 3 4 5 6 7 8 V<sub>R</sub>, REVERSE VOLTAGE (V)

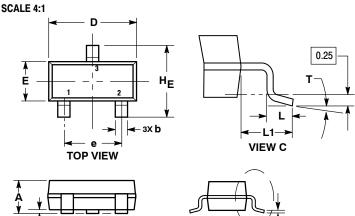
Figure 3. Capacitance





SOT-23 (TO-236) CASE 318-08 **ISSUE AS** 

**DATE 30 JAN 2018** 



SEE VIEW C

**END VIEW** 

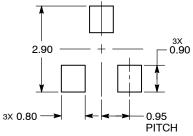
- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
  MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF
- THE BASE MATERIAL.

  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

#### **RECOMMENDED SOLDERING FOOTPRINT**

SIDE VIEW



DIMENSIONS: MILLIMETERS

#### **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: PIN 1. ANODE 2. CATHODE 3. CATHODE-ANODE	PIN 1. CATHODE 2. CATHODE	STYLE 13: PIN 1. SOURCE 2. DRAIN 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	STYLE 17: PIN 1. NO CONNECTION 2. ANODE 3. CATHODE	STYLE 18: PIN 1. NO CONNECTION 2. CATHODE 3. ANODE	STYLE 19: PIN 1. CATHODE 2. ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 24: PIN 1. GATE 2. DRAIN 3. SOURCE	STYLE 25: PIN 1. ANODE 2. CATHODE 3. GATE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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