



100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
100V	$22m\Omega$ @ $V_{GS} = 10V$	51.7A
	$30m\Omega$ @ $V_{GS} = 6V$	44.3A
	43.7mΩ @ V _{GS} = 4.5V	36.7A

Description

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power Management Functions
- DC-DC Converters
- Backlighting

Features

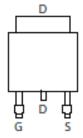
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_G Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

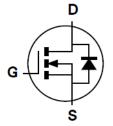
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.33 grams (Approximate)



Top View



Pin Out Top View



Equivalent Circuit

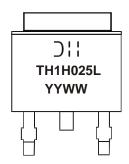
Ordering Information (Note 5)

Part Number	Case	Packaging
DMTH10H025LK3Q-13	TO252 (DPAK)	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Please refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



Dil = Manufacturer's Marking
TH1H025L = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 = 2018)
WW = Week Code (01 to 53)



Maximum Ratings $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	100	V	
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current, $V_{GS} = 10V$ (Note 7) $T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$		I _D	51.7 36.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	95	A	
Maximum Continuous Body Diode Forward Current (Note 7)		Is	77	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I _{SM}	95	A
Avalanche Current, L = 0.1mH	I _{AS}	15.8	A	
Avalanche Energy, L = 0.1mH		E _{AS}	12.5	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		P _D	3.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	48	°C/W
Total Power Dissipation (Note 7)	P _D	100	W	
Thermal Resistance, Junction to Case (Note 7)		$R_{ heta JC}$	1.5	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	100		_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	1	_	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1	I	3	٧	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		1	17.1	22		$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	21.4	30	$m\Omega$	$V_{GS} = 6V, I_D = 20A$	
		1	28.3	43.7		$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	1	1	1.3	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	1	1477	_		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	-	263	_	pF		
Reverse Transfer Capacitance	Crss	1	20	_		1 = 11V11 12	
Gate Resistance	R_{G}	1	1.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_G	1	21	_		V 50V I 00A	
Gate-Source Charge	Q_{GS}	1	5.7	_	nC	$V_{DD} = 50V, I_D = 20A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{GD}	1	3.8	_			
Turn-On Delay Time	t _{D(ON)}	1	6.3	_		V _{DD} = 50V, V _{GS} = 10V,	
Turn-On Rise Time	t _R	1	9.4	_			
Turn-Off Delay Time	t _{D(OFF)}	1	16.7	_	ns	$I_D = 20A$, $R_G = 6\Omega$	
Turn-Off Fall Time	t _F	_	8.2				
Reverse Recovery Time	t _{RR}	_	38.7	_	ns	1 200 1:/14 1000/	
Reverse Recovery Charge	Q _{RR}	_	53.7	_	nC	I _F = 20A, di/dt = 100A/μs	

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

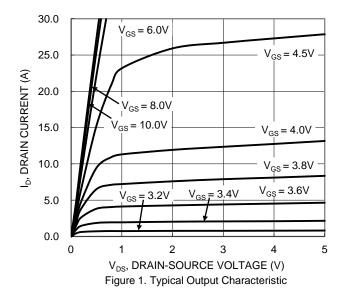
7. Thermal resistance from junction to soldering point (on the exposed drain pad).

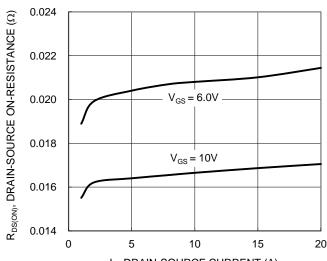
8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.









I_D, DRAIN-SOURCE CURRENT (A) Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

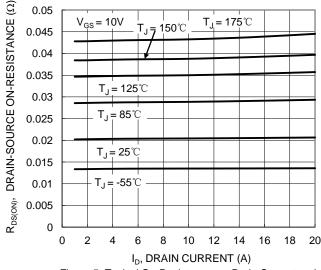


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

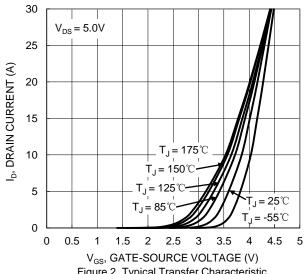


Figure 2. Typical Transfer Characteristic

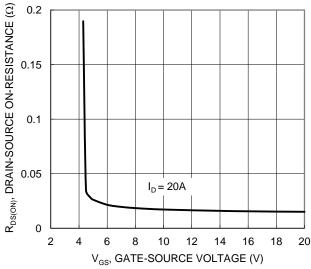


Figure 4. Typical Transfer Characteristic

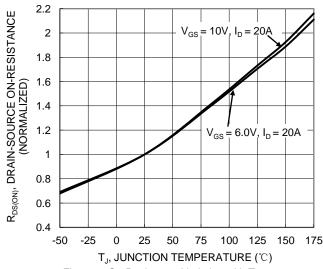


Figure 6. On-Resistance Variation with Temperature





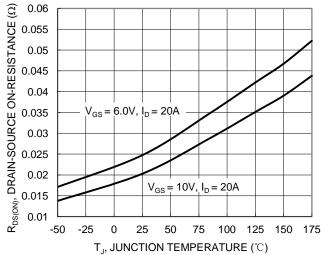


Figure 7. On-Resistance Variation with Temperature

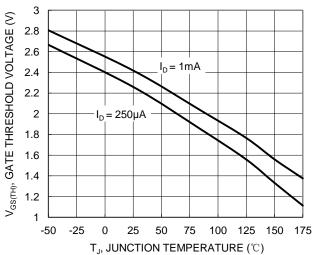


Figure 8. Gate Threshold Variation vs. Junction Temperature

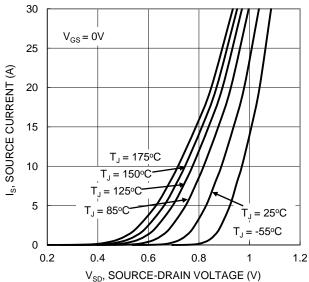
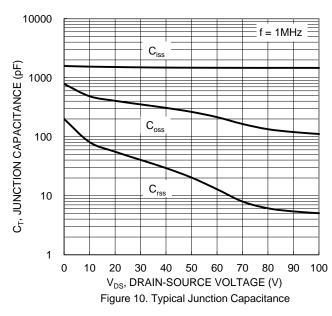
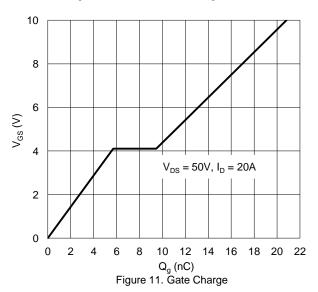


Figure 9. Diode Forward Voltage vs. Current





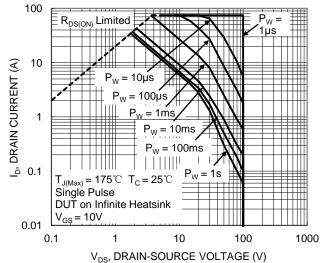


Figure 12. SOA, Safe Operation Area



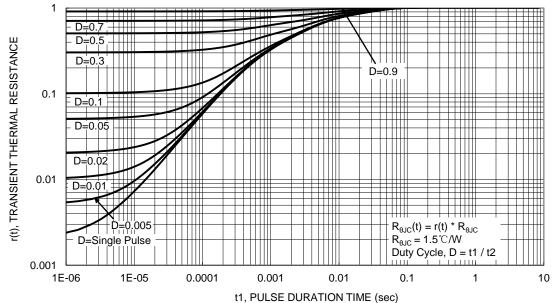


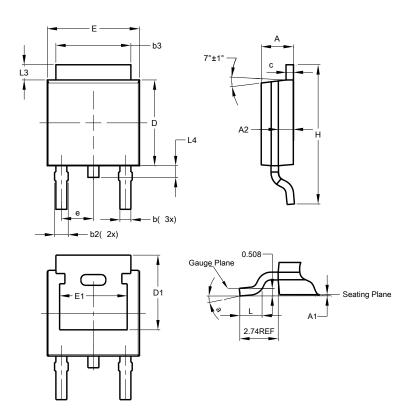
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)

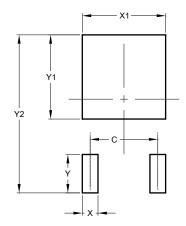


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A 1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Υ	2.600		
Y1	5.700		
Y2	10.700		



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